ABSTRACT OF THE DISCLOSURE

A bus (3) includes a data bus (3a) and a clock bus (3b). The data bus (3a) is used for propagation of data MDIO conforming to the MDIO interface standards performed between a host controller IC (40) and a transceiver IC (1), and for propagation of data (SDA) conforming to the I^2C standards performed between the transceiver IC (1) and a peripheral IC (2). Meanwhile, the clock bus (3b) is used for propagation of clock (MDC) conforming to the MDIO interface standards performed between the host controller IC (40) and the transceiver IC (1), and for propagation of clock (SCL) conforming to the I^2C standards performed between the transceiver IC (1) and the peripheral IC (2).